

# Interprocedural register allocation for the WAM based on source-to-source transformations.

Ulrich Neumerkel

Institut für Computersprachen

Technische Universität Wien

ulrich@mips.complang.tuwien.ac.at

- new intermediate language — Continuation Prolog
- transformation driven optimization, global analysis not required
- improved code generation for WAM and BinWAM
- minimal extension of WAM-code generation integrated into SICStus

# Problems with current implementations

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$$\begin{array}{l}
 e \longrightarrow \text{"t"}. \\
 e \longrightarrow \text{"o"}, e, e.
 \end{array}
 \quad
 \begin{array}{c}
 \xrightarrow{X_s} \\
 e([0't|X_s], X_s). \\
 \xrightarrow{X_{s0}} \quad \xrightarrow{X_{s1}} \\
 e([0'o|X_{s0}], X_s) \leftarrow e(X_{s0}, X_{s1}), e(X_{s1}, X_s). \\
 \xleftarrow{X_s}
 \end{array}$$

Problems in WAM:

- short lifetime of registers
- argument register bottleneck

$$e([0'o|\overbrace{X_{s0}}, \overbrace{X_{s1}}], X_s) \leftarrow \underbrace{e(X_{s0}, X_{s1}), e(X_{s1}, X_s)}_{X_s}. \quad \% \quad \text{lifetime of variables disjoint}$$

- cannot be solved by mode analysis (influence of usage, e.g.  $\leftarrow e(X_s, [])$ .)
- cannot be solved by fold/unfold (lacks context between goals)

# Continuation Prolog

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$$\begin{array}{l} e([0't|Xs],Xs). \\ e([0'o|Xs0],Xs) \leftarrow \\ \quad e(Xs0,Xs1), \\ \quad e(Xs1,Xs). \end{array} \quad \begin{array}{l} [ e([0't|Xs],Xs) ] \leftarrow \\ \quad [] \\ [ e([0'o|Xs0],Xs) ] \leftarrow \\ \quad [ e(Xs0,Xs1), \\ \quad e(Xs1,Xs) ]. \end{array}$$

- clauses: multiple heads — multiple goals, order significant

$$\begin{array}{l} [a,b] \leftarrow \\ \quad [c]. \end{array}$$
$$\leftarrow \text{inferencediff}([a,b,d], [c,d]).$$

# Outline of interprocedural register allocation

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contprolog (CProg0) ←

contprolog\_ebctransformed (CProg0,CProg) % EBC-Transformation

(Prolog→) Continuationprolog→ Transformed

# Outline of interprocedural register allocation

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contprolog\_llprologallocated (CProg0, LLProg) ←

contprolog\_ebctransformed (CProg0, CProg), % EBC-Transformation

contprolog\_llprolog (CProg, LLProg). %

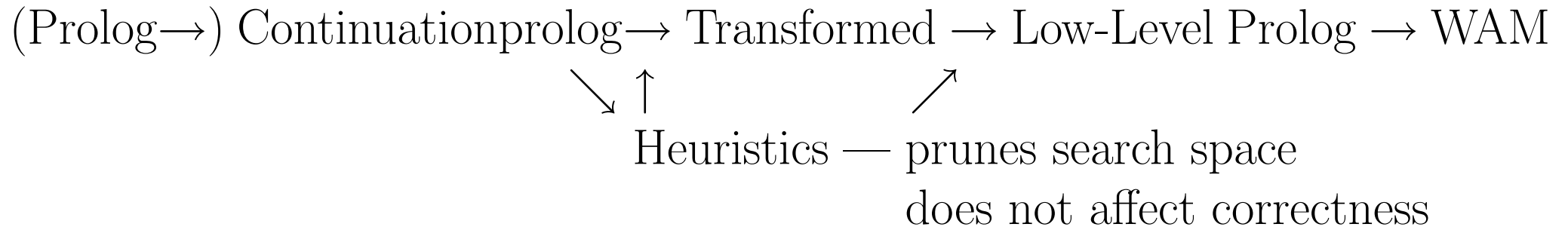
(Prolog →) Continuationprolog → Transformed → Low-Level Prolog → WAM

# Outline of interprocedural register allocation

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```

contprolog_llprologallocated_(CProg0, LLProg, Anns) ←
  contprolog_(CProg0, Anns), % Heuristics
contprolog_ebctransformed_(CProg0, CProg, Anns), % EBC-Transformation
contprolog_llprolog_(CProg, LLProg, Anns). %
  
```

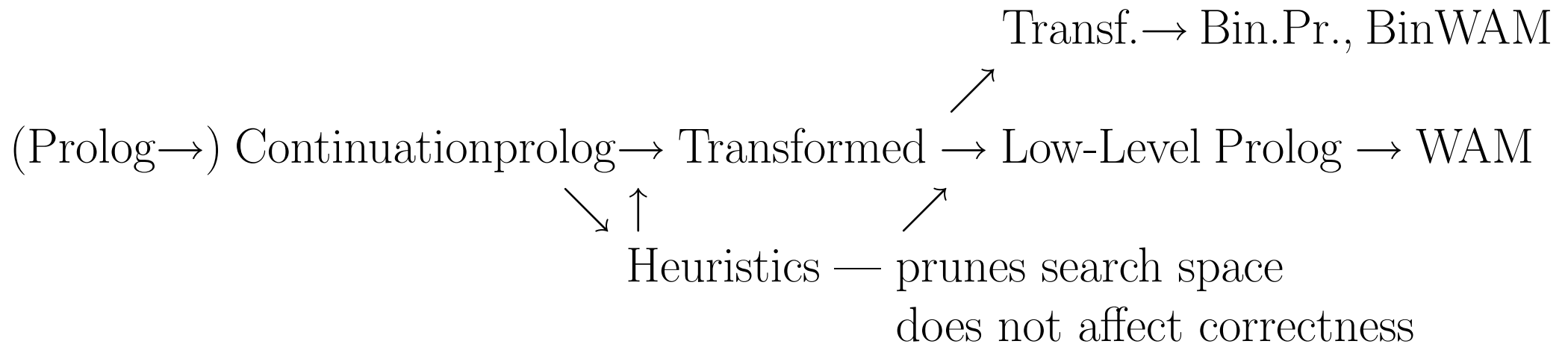


1, 2, 3,

# Outline of interprocedural register allocation

---

contprolog\_llprologallocated\_(CProg0, LLProg, Anns) ←  
contprolog\_(CProg0, Anns), % Heuristics  
contprolog\_ebctransformed\_(CProg0,CProg, Anns), % EBC-Transformation  
contprolog\_llprolog\_(CProg, LLProg, Anns). %



1, 2, 3, 4.

# EBC- (*equality based continuation*) transformation system

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Preserves operational equivalence of Continuation Prolog programs

## Equations $s \doteq t$ for alternative representations

$u \cdot s\theta \cdot v$  is equivalent to  $u \cdot t\theta \cdot v$

$[\text{old}(a_1, \dots, a_n, b_1, \dots, b_m)] \doteq [\text{new1}(a_1, \dots, a_n), \text{new2}(b_1, \dots, b_m)]$   $a_i, b_i$  distinct vars

## Compilation

**body**  $= u \cdot s\theta \cdot v$  is translated into  $u \cdot t\theta \cdot v$

**heads** All heads are rewritten.

add interface predicate (required for meta-calls and entry-points)

$[\text{old}(a_1, \dots, a_n, b_1, \dots, b_m)] \leftarrow [\text{new1}(a_1, \dots, a_n), \text{new2}(b_1, \dots, b_m)]$

**Simplification**  $h \cdot r \leftarrow b \cdot r$  simplifies to  $h \leftarrow b$

if  $r$  contains only variables not in  $h$  and  $b$



## Example: Translation of arithmetical expressions

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%	% Output unifications regrouped
$\text{expr}(A+B,T) \longrightarrow$	$[\text{expr}(A+B,T,Xs0,Xs)] \longleftarrow$
$\text{expr}(A,TA),$	$[\text{expr}(A,TA,Xs0,Xs1),$
$\text{expr}(B,TB),$	$\text{expr}(B,TB,Xs1,Xs2),$
$[\text{'add'}(TA,TB,T)].$	$Xs2 = [\text{'add'}(TA,TB,Tx) Xs3],$
	$[Tx,Xs3] = [T,Xs]$
	$].$

## Example: Splitting output arguments

---

$[\text{expr}(\text{E}, \text{T}, \text{Xs0}, \text{Xs})] \doteq [\text{expr1}(\text{E}, \text{Xs0}), \text{r}(\text{T}, \text{Xs})].$

$[[\text{T}_x, \text{Xs0}] = [\text{T}, \text{Xs}]] \doteq [=([\text{T}_x, \text{Xs0}]), \text{r}(\text{T}, \text{Xs})].$

$[\text{expr}(\text{E}, \text{T}, \text{Xs0}, \text{Xs})] \leftarrow$   $[\text{expr1}(\text{A}+\text{B}, \text{Xs0}), \text{r}(\text{T}, \text{Xs})] \leftarrow$   
[ expr1(E, Xs0),  
r(T, Xs)  
].

$[[\text{T}, \text{Xs}]] \leftarrow$   $\text{Xs2} = [\text{'add'}(\text{TA}, \text{TB}, \text{T}_x) | \text{Xs3}],$   
[[  
].

## Example: Splitting output arguments and simplification

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$$[\text{expr}(E, T, Xs0, Xs)] \doteq [\text{expr1}(E, Xs0), r(T, Xs)].$$

$$[[T_x, Xs0] = [T, Xs]] \doteq [=([T_x, Xs0]), r(T, Xs)].$$

$$\begin{array}{l}
 [\text{expr}(E, T, Xs0, Xs)] \leftarrow \\
 \quad [ \text{expr1}(E, Xs0), \\
 \quad \quad r(T, Xs) \\
 \quad ]. \\
 [=([T, Xs]), r(T, Xs)] \leftarrow \\
 \quad [].
 \end{array}
 \quad
 \begin{array}{l}
 [\text{expr1}(A+B, Xs0), \overline{r(T, Xs)}] \leftarrow \\
 \quad [ \text{expr1}(A, Xs0), \\
 \quad \quad r(TA, Xs1), \\
 \quad \quad \text{expr1}(B, Xs1), \\
 \quad \quad r(TB, Xs2), \\
 \quad \quad Xs2 = [\text{'add'}(TA, TB, T_x) | Xs3], \\
 \quad \quad =([T_x, Xs3]), \\
 \quad \quad \overline{r(T, Xs)} \\
 \quad ].
 \end{array}$$

1, 2.

## Continuation Prolog to low-level Prolog.

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- AND-control mapped onto stack
- only a subset can be translated
  - Predicate functors. Occur as the first element in the head.
  - Continuation functors. Occur as the second element in the head.

must be disjoint

- empty body for clauses with continuation functor in the head
- continuation functor must be the same for all clauses of the same predicate

$$\begin{aligned} & [ e(Xs), r(Xs0) ] \leftarrow \\ & [ e(Xs1), r(Xs0), e(Xs), r(Xs1) ]. \end{aligned}$$

## Example: translation into low-level Prolog

---

```
[expr1(A+B,Xs0)] ←  
  [ expr1(A,Xs0),  
    r(TA,Xs1),  
    expr1(B,Xs1),  
    r(TB,Xs2),  
    Xs2 = ['add'(TA,TB,Tx)|Xs3],  
    =([Tx,Xs3])  
  ].
```

```
expr1(A+B,Xs0) ←  
  expr1(A,Xs0),  
  'SOURCE'+TA,+Xs1),  
  expr1(B,Xs1),  
  'SOURCE'+TB,+Xs2),  
  Xs2 = ['add'(TA,TB,T)|Xs3],  
  'SINK'+T,+Xs3).
```

# Low level-Prolog to WAM-code

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## Two low level built-ins.

- 'SINK'/ $n$ : ensure that  $n$  values are in registers
- 'SOURCE'/ $n$ : assume that  $n$  values are in registers

	$[\text{pop}(\text{C}, \text{P})] \leftarrow$ $[\text{pop}(\text{C}),$ $\text{r}(\text{P})].$	$\text{pop}(\text{C}, \text{P}) \leftarrow$ $\text{pop}(\text{C}),$ 'SOURCE'(-, +P).	$[\text{allocate}$ $, \text{get\_y\_variable}(0, 1)$ $, \text{init}([])$ $, \text{call}(\text{pop}/1, 1)$ $, \text{get\_y\_value}(0, 1)$ $, \text{deallocate}$ $, \text{execute}(\text{true}/0)].$
$\text{pop}(\text{china}, 8250).$	$[\text{pop}(\text{china}), \text{r}(8250)] \leftarrow$ $[\ ].$	$\text{pop}(\text{china}) \leftarrow$ 'SINK'(-, +(8250)).	$[\text{get\_constant\_x0}(\text{china})$ $, \text{put\_constant}(8250, 1)$ $, \text{proceed}].$
$\text{pop}(\text{india}, 5863).$ ... .	$[\text{pop}(\text{india}), \text{r}(5863)] \leftarrow$ $[\ ].$ ... .	$\text{pop}(\text{india}) \leftarrow$ 'SINK'(-, +(5863)). ... .	$[\text{get\_constant\_x0}(\text{india})$ $, \text{put\_constant}(5863, 1)$ $, \text{proceed}].$

# Final example code

% Prolog code	% Original WAM	% Optimized WAM	% ll-Prolog
<pre> expr(I, T, Xs0,Xs) ← integer(I), !, I = T, Xs0 = Xs. </pre>	<pre> [builtin(integer(0),else) ,cutb ,get_x_val(1,0) ,get_x_val(2,3) ,proceed]. </pre>	<pre> [builtin(integer(0),else) ,cutb ,proceed]. </pre>	<pre> expr1(I, Xs0) ← integer(I), !, 'SINK'(+I, +Xs0). </pre>
<pre> expr(A+B, T, Xs0, Xs) ← </pre>	<pre> [get_str_x0((+)/2) ,allocate ,get_y_var(4,3) ,get_y_var(3,1) ,unify_x_var(0) ,unify_y_var(5) ,put_y_var(1,1) ,put_y_var(6,3) ,init([0,2]) ,call(expr/4,7) ,put_y_val(5,0) ,put_y_first_val(2,1) ,put_y_unsafe_val(6,2) ,put_y_first_val(0,3) ,call(expr/4,5) ,put_y_val(0,0) ,get_list(0) ,unify_temp_var(0) ,unify_y_local_val(4) ,get_temp_str('add'/3,0) ,unify_y_local_val(1) ,unify_y_local_val(2) ,unify_y_local_val(3) ,deallocate ,execute(true/0)]. </pre>	<pre> [get_str_x0((+)/2) ,unify_x_var(0) ,allocate ,unify_y_var(1)  ,init([0]) ,call(expr1/2,2) ,get_y_first_val(0,0)%%  ,put_y_val(1,0)%% ,call(expr1/2,1)  ,get_list(1) ,unify_temp_var(2) ,unify_x_var(1) ,get_temp_str('add'/3,2) ,unify_y_local_val(0) ,unify_x_local_val(0) ,unify_x_var(0) ,deallocate ,execute('SINK'/2)]. </pre>	<pre> % ll-Prolog expr1(A+B, Xs0) ← expr1(A, Xs0), 'SOURCE'+(+TA,+Xs1), expr1(B, Xs1), 'SOURCE'+(+TB,+[ T   Xs]), 'SINK'+(+T,+Xs). </pre>

+ fewer instructions

+ simpler instructions (less trail checking,  
fewer general unifications)

+ 30% faster

+ smaller environments (7+4 vs. 2+1 vars)

+ fewer argument registers (4 vs. 2)

## Blocked goals/Constraints

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- life registers in call, execute and heapmargin\_call
- Simulating block

```
← block predicate(-, ...).  
predicate(I1, ...) ←  
    var(I1),  
    !,  
    blockable_predicate(I1,..., O1,...),  
    'SINK'(+O1,...).  
predicate(_I1, ...) ←  
    ... .
```

```
← block blockable_predicate(-, ?, ?, ?).  
blockable_predicate(I1,..., O1,...) ←  
    predicate(I1,...),  
    'SOURCE'(+O1,...).
```



# Direct compilation of DCGs

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a  $\longrightarrow$   
[].

a( $X_S$ )  $\longleftarrow$   
'SINK'(-,+ $X_S$ ).

b(1)  $\longrightarrow$   
[].

b(1,  $X_S$ )  $\longleftarrow$   
'SINK'(-,+ $X_S$ ).

c(1,2)  $\longrightarrow$   
[].

c(1,  $X_S$ , 2)  $\longleftarrow$   
'SINK'(-,+ $X_S$ ).

$$\begin{aligned} \text{qsorted}([E|Es]) &\longrightarrow \\ &\{ \text{partition}(Es, E, Es1, Es2) \}, \\ &\text{qsorted}(Es1), \\ &[E], \\ &\text{qsorted}(Es2). \\ \text{qsorted}([]) &\longrightarrow \\ &[]. \end{aligned}$$

$$\begin{aligned} \text{qsorted}([E|Es], Xs0) &\longleftarrow \\ &\text{partition}(Es, E, Es1, Es2), \\ &\text{qsorted}(Es1, Xs0), \\ &\text{'SOURCE'}(-, +[E|Xs1]), \\ &\text{qsorted}(Es2, Xs1). \\ \text{qsorted}([], Xs0) &\longleftarrow \\ &\text{'SINK'}(-, +Xs0). \end{aligned}$$

$$\begin{aligned} \text{phrase\_qsorted}(Es, Xs0, Xs) &\longleftarrow \\ &\text{qsorted}(Es, Xs0), \\ &\text{'SOURCE'}(-, +Xs). \end{aligned}$$

# Benchmark results

no code duplication, number of WAM instructions reduced

existing				artificial			
	modif/orig	EBC/modif	tuned/modif		modif/orig	EBC/modif	tuned/modif
expr	1.53	1.20	1.30	e	—	1.09	—
fact 10	—	1.06	2.05	e+1	—	1.55	—
fact 100	—	1.02	1.05	e+2	—	1.99	—
qsort	1.00	1.02	1.14	e+3	—	2.29	—
query	—	1.11	1.21	de	—	1.27	—
serial	—	1.00	1.16	de+1	—	1.67	—
d-divide	1.18	—	1.03	de+2	—	2.13	—
d-log	1.55	—	1.00	de+3	—	2.41	—
d-ops	1.25	—	1.05				
d-times	1.18	—	1.05				
chat	—	1.23	1.31				

□

## Related Work

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**Mode analysis:** tied to actual usage & control strategy

- depends on control strategy
- depends on actual usage
- + optimizes not steadfast programs

**Aquarius Prolog:** uninitialized register conversion

**Output value placement:** cost model to minimize tradeoffs LCO/registers

**Destructive updates:** HAG (Hidden Accumulator Grammar) uses back-trackable destructive assignment

- coroutining impossible
- EBC+register trailing produces same effect without destructive assignment

# Limitations

---

Steadfastness required

$\text{expr}((X \text{ is Expr}), \text{Code}) \leftarrow$   
 $\text{phrase}(\text{expr}(\text{Expr}, X), \text{Code}).$

$\text{expr}(V, V) \longrightarrow \{\text{var}(V)\}, !. \% \text{ blocking optimization}$

$\text{expr}(I, I) \longrightarrow \{\text{integer}(I)\}, !. \% \text{ blocking optimization}$

$\text{expr}(A+B, T) \longrightarrow \text{expr}(A, TA), \text{expr}(B, TB), ['\text{add}'](TA, TB, T).$

$\text{expr}(A-B, T) \longrightarrow \text{expr}(A, TA), \text{expr}(B, TB), ['\text{sub}'](TA, TB, T).$

$\text{expr}(A*B, T) \longrightarrow \text{expr}(A, TA), \text{expr}(B, TB), ['\text{mul}'](TA, TB, T).$

$\text{expr}(A/B, T) \longrightarrow \text{expr}(A, TA), \text{expr}(B, TB), ['\text{div}'](TA, TB, T).$

$\leftarrow \text{expr}((1 \text{ is Expr}), \text{Code}).$

# Limitations

---

Steadfastness required

$\text{expr}((X \text{ is Expr}), \text{Code}) \leftarrow$   
 $\text{phrase}(\text{expr}(\text{Expr}, X), \text{Code}).$

~~$\text{expr}(V, V) \Longrightarrow \{\text{var}(V)\}, !. \%$  blocking optimization~~

$\text{expr}(V, T) \longrightarrow \{\text{var}(V)\}, !, \{V = T\}.$

~~$\text{expr}(I, I) \Longrightarrow \{\text{integer}(I)\}, !. \%$  blocking optimization~~

$\text{expr}(I, T) \longrightarrow \{\text{integer}(I)\}, !, \{I = T\}.$

$\text{expr}(A+B, T) \longrightarrow \text{expr}(A, \text{TA}), \text{expr}(B, \text{TB}), [\text{'add'}(\text{TA}, \text{TB}, \text{T})].$

$\text{expr}(A-B, T) \longrightarrow \text{expr}(A, \text{TA}), \text{expr}(B, \text{TB}), [\text{'sub'}(\text{TA}, \text{TB}, \text{T})].$

$\text{expr}(A*B, T) \longrightarrow \text{expr}(A, \text{TA}), \text{expr}(B, \text{TB}), [\text{'mul'}(\text{TA}, \text{TB}, \text{T})].$

$\text{expr}(A/B, T) \longrightarrow \text{expr}(A, \text{TA}), \text{expr}(B, \text{TB}), [\text{'div'}(\text{TA}, \text{TB}, \text{T})].$

$\leftarrow \text{expr}((1 \text{ is Expr}), \text{Code}).$

# Conclusion

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- supports differences, and steadfast programs
- simulating states gets cheaper
- independent of usage of predicates — optimization of libraries possible
- supports Prolog with blocked goals, constraints

## Further work

- adaptations of formalisms like EDCGs (require no analysis)
- extending the lifetime of registers
  - allocation of context arguments
  - caller/callee saved registers